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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/603,893	06/24/2003	Tarmo Ruotsalainen	872.0140.U1(US)	4891
29683	7590 02/12/2004		EXAMINER	
	ON & SMITH, LLP		YOUNG, BRIAN K	
4 RESEARCH SHELTON, (	T 06484-6212		ART UNIT	PAPER NUMBER
,			2819	
			DATE MAILED: 02/12/2004	1

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No.	Applicant(s)	0
		10/603,893	RUOTSALAINEN ET AL.	
Office Action Summary		Examin r	Art Unit	
		Brian Young	2819	
Period fo	Th MAILING DATE of this communication or Reply	app ars on the cov r sh t	with the correspond nce address	
THE - Exte after - If the - If NO - Failt Any	MAILING DATE OF THIS COMMUNICATION OF SIX (6) MONTHS from the mailing date of this communication of period for reply specified above, the maximum statutory per use to reply within the set or extended period for reply will, by some period for reply will be	ON. R 1.136(a). In no event, however, may b. a reply within the statutory minimum of t riod will apply and will expire SIX (6) M latute, cause the application to become	a reply be timely filed  nirty (30) days will be considered timely.  NTHS from the mailing date of this communication.  ABANDONED (35 U.S.C. § 133).	
Status				
1) 又	Responsive to communication(s) filed on 2	4 June 2003.		
·		This action is non-final.		
3)□	Since this application is in condition for allo closed in accordance with the practice und	owance except for formal ma		
Disposit	ion of Claims			
5)⊠ 6)⊠ 7)□ 8)□	Claim(s) 1-16 is/are pending in the applica 4a) Of the above claim(s) is/are with Claim(s) 7-11 and 13 is/are allowed. Claim(s) 1-6,12 and 14 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and tion Papers	drawn from consideration.		
91	The specification is objected to by the Exar	niner		
•	The drawing(s) filed on <u>24 June 2003</u> is/are		jected to by the Examiner.	
,	Applicant may not request that any objection to			
11)	Replacement drawing sheet(s) including the co The oath or declaration is objected to by th	·		
Priority :	under 35 U.S.C. § 119			
a)	Acknowledgment is made of a claim for form All b) Some * c) None of:  1. Certified copies of the priority docum 2. Certified copies of the priority docum 3. Copies of the certified copies of the application from the International Buse the attached detailed Office action for a	nents have been received. nents have been received in priority documents have been reau (PCT Rule 17.2(a)).	Application No en received in this National Stage	
Attachmer	• •	A) 🔲 Interview	v Summary (PTO-413)	
	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-948	) Paper N	o(s)/Mail Date	
3) 🛛 Infor	mation Disclosure Statement(s) (PTO-1449 or PTO/SE er No(s)/Mail Date <u>8/27/03</u> .		f Informal Patent Application (PTO-152)	

Application/Control Number: 10/603,893 Page 2

Art Unit: 2819

The following is a quotation of the second paragraph of 35 U.S.C. 112:
 The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

2. Claims 1-6,12 and 14-16 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 1, line 6, recites the term "the signal transistor" which lacks antecedent basis.

Claims 6 and 12 recite "one dimension of the transistor" which is overly vague.

Claim 14 recites "creating an imbalance" which is overly vague.

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1, 2 and 5 are rejected under 35 U.S.C. 102(b) as being anticipated by Koifman et al.

Koifman et al disclose an analog-to-digital converter (FIGS. 9A-B are simplified block diagrams illustrating a delta-sigma analog-to-digital converter) which operates as an oversampled delta-sigma converter. The converter is implemented fully differentially, having doubled integrator capacitors (130, 230), comparators (180, 280), and feedback units (160, 260). In order to reduce the influence of parasitic capacities, the feedback units (160, 260) comprise cascoded switches (171-179). The analog-to-digital converter (500) comprises

Application/Control Number: 10/603,893

Art Unit: 2819

multiple comparators (580, 572, 573, 574), dynamic matching circuits (801, 802). The comparators (572, 573, 574) can receive *dithered* input signals. FIG. 13 is a simplified block diagram illustrating the superimposition of *dither* signals to currents sources in the converter of FIGS. 9A-B. FIG. 13 is a simplified block diagram illustrating the superimposition of *dither signals*. Current sources 554, 553, 552, respectively, of reference bank 550 of comparator 500. In FIG. 13, modified reference bank 550 is denoted by primed reference number 550'. Transistors 552', 553', 554' are coupled in parallel to current sources 554,553, and 552, respectively. Transistors 552', 553', 554' have control inputs (e.g., gates) for receiving dither signals. Transistors 552', 553', and 554' are collectively referred to as means for superimposing dither.

- 5. Claims 12 and 14 would be allowable if rewritten or amended to overcome the rejection(s) under 35 U.S.C. 112, second paragraph, set forth in this Office action.
- 6. Claims 7-11 and 13 are allowed.
- 7. Claims 3,4 and 6 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, second paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims.
- 8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Yamakido et al disclose an over-sampling analog-to-digital converter using a current switching circuit as a local digital-to-analog converter using dither.

Art Unit: 2819

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian Young whose telephone number is 571-272-1816.

The examiner can normally be reached on Mon-Fri 7:30-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mike Tokar can be reached on 571-272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

Brian Young

Primary Examiner

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